Sheryl P. Walter Declassified/Released US Department of State EO Systematic Review 20 Mar 2014

Message Text

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PAGE 01 STATE 054100
ORIGIN EB-08

INFO OCT-01 EUR-12 ISO-00 DODE-00 NSAE-00 USIA-06
TRSE-00 SOE-02 DOE-11 CIAE-00 COME-00 /040 R

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C O N F I D E N T I A L STATE 054100

USOECD, EXCON

E.O. 11652: XGDS-1

TAGS: ESTC, COCOM, CZ, US

SUBJECT: US PORTABLE LOGIC CIRCUIT ANALYZER TO CZECHOSLOVAKI
IL 1355

REF: A) COCOM DOC (77) 457, B) PARIS 20621,
C) STATE 187861 AND 246393, D) PARIS 31620

THE FOLLOWING INFO WAS AIRPOUCHED TO USDEL WEEK OF FEB.13.
USDEL REQUESTED TO SEND STATUS REPORT ON CASE.

1. THE UK AUTHORITIES STATE THAT IN THEIR VIEW ELECTRONIC
TEST INSTRUMENTS DESIGNED FOR THE TESTING OF PRINTED
CIRCUIT BOARDS AND LIMITED TO THE TESTING OF PRINTED
CIRCUIT BOARDS ARE NOT COVERED BY IL 1355. IT IS NOT
ENTIRELY CLEAR WHETHER OR NOT THIS UK VIEW IS INTENDED TO
APPLY TO EITHER THE US DATA 2400B TESTER UNDER CONSIDERATION,
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PAGE 02 STATE 054100

OR THE BRITISH OWN MEMBRAIN MB 2420.

2. IN ANY EVENT, THE US BELIEVES THAT BOTH THE DATATEST
2400B AND THE MEMBRAIN MB 2420 ARE NOT ONLY CAPABLE OF
CHECKING THE CONTINUITY OF THE BASIC INTERCONNECTION
BOARD WIRING, BUT ARE PRIMARILY DESIGNED FOR EXERCISING
THE BOARD MOUNTED DIGITAL COMPONENTS, BOTH EMBARGOED AND

Sheryl P. Walter Declassified/Released US Department of State EO Systematic Review 20 Mar 2014

Sheryl P. Walter Declassified/Released US Department of State EO Systematic Review 20 Mar 2014

NONEMBARGOED TYPES, BY CONDUCTING A TRUTH TABLE TEST OF A CIRCUIT. FURTHERMORE, BOTH TEST SETS ARE EQUIPPED WITH FAULT DETECTION AND ISOLATION CLIPS WHICH WILL ALLOW AN OPERATOR TO MAKE A FUNCTIONAL TEST OF ANY INDIVIDUAL DIGITAL INTEGRATED CIRCUIT ON THE BOARD. IT IS NOTED THAT THE USER OF THESE TEST SETS, WITH A BIT OF INGENUITY, COULD PROVIDE AN APPROPRIATE TEST FIXTURE FOR THE EXERCISING OF A DIGITAL DEVICE OF HIS PARTICULAR INTEREST, I.E., A MEMORY OR A MICROPROCESSOR DEVICE, AND FUNCTIONALLY EXERCISE THROUGH AN APPROPRIATE TEST PATTERN. THIS FUNCTIONAL EXERCISING IS PRECISELY WHAT US MANUFACTURERS ARE NOW DOING TO FINISHED DIGITAL INTEGRATED CIRCUIT DEVICES AS A FINAL TEST BEFORE SHIPMENT TO CUSTOMERS. IN THE US VIEW, THESE TESTERS ARE COVERED BY IL 1355(B).

3. IN SUMMARY, THE US SHOULD REINTERATE THE VIEW THAT THE DATATEST 2400B IS COVERED BY IL 1355(B) BECAUSE IT IS CABLE OF EXERCISING BOTH ELECTRONIC CIRCUITS CONTAINING EMBARGOED DIGITAL INTETRATED CIRCUITS, AND INDIVIDUAL EMBARGOED MICROCIRCUITS (GIVEN APPROPRIATE TEST ADAPTERS AND FIXTURES IN THE LATTER CIRCUMSTANCE). PLEASE OBSERVE THAT IL 1355 COVERS DESCRETE MICROCIRCUIT AND MICROCIRCUIT ASSEMBLY TEST EQUIPMENT OF ALL TYPES AND DOES NOT SELECTIVELY COVER THOSE TEST SETS EMPLOYING PARAMETRIC MEASUREMENT TECHNIQUES WHILE FREEING FROM CONTROL TEST INSTRUMENTS DESIGNED TO FUNCTIONALLY
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EXERCISE A CIRCUIT THROUGH A TRUTH TABLE TYPE SEQUENCE.

4. ON THE OTHER HAND, THE US TECHNICIANS AGREE WITH THE UK THAT TESTERS FOR WIRING HARNESS, BACK PLANE, AND/OR BARE PRINTED CIRCUIT BOARD CONDUCTIVITY TESTING ARE NOT COVERED BY IL 1355. CHRISTOPHER

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Sheryl P. Walter Declassified/Released US Department of State EO Systematic Review 20 Mar 2014

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Decaption Note:
Disposition Action: RELEASED
Disposition Approved on Date:
Disposition Case Number: n/a
Disposition Comment: 25 YEAR REVIEW
Disposition Date: 20 Mar 2014
Disposition Event:
Disposition History: n/a
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Reference: 78 PARIS 20621
Retention: 0
Review Action: RELEASED, APPROVED
Review Content Flags:
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Review Event:
Review Exemptions: n/a
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Review Withdrawn Fields: n/a
SAS ID: 3137274
Secure: OPEN
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Subject: US PORTABLE LOGIC CIRCUIT ANALYZER TO CZECHOSLOVAKI IL 1355
TAGS: ESTC, CZ, US, COCOM
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Type: TE
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Sheryl P. Walter
Declassified/Released
US Department of State
EO Systematic Review
20 Mar 2014
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